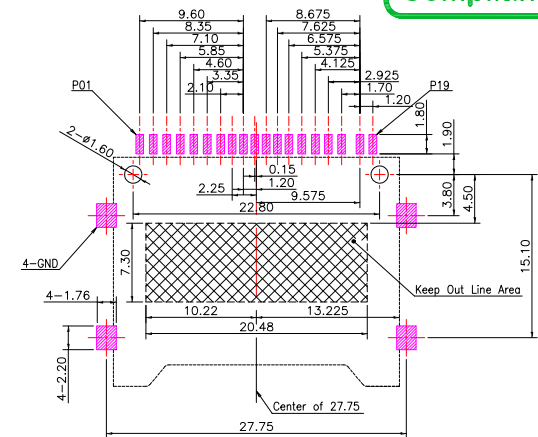
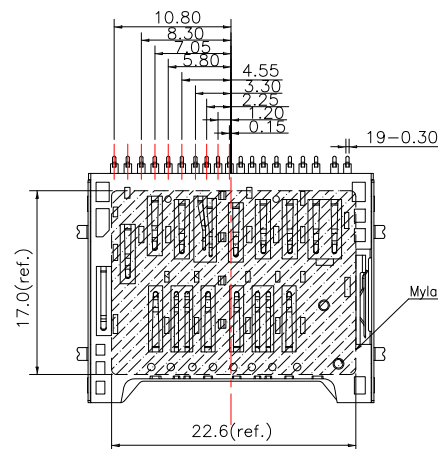
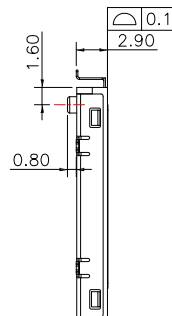
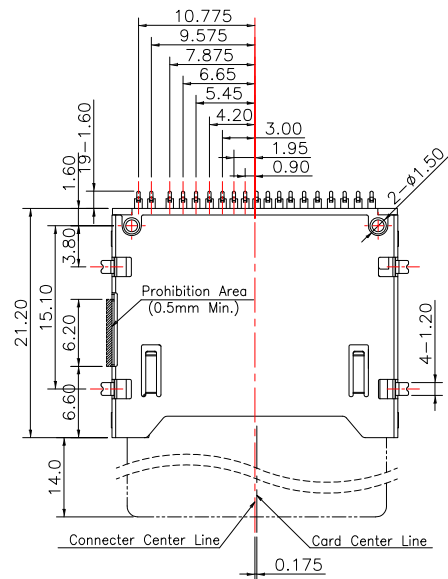




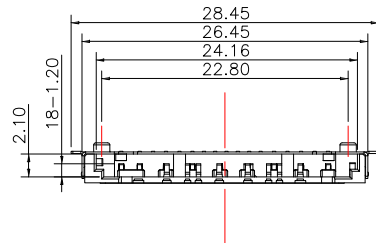
PCB LAYOUT

Pin Assignment

Pin#	Name	Description	Pin#	Name	Description
P01	VSS3	Supply Voltage Ground	P11	VDD1	Supply Voltage (3.3V)
P02	DAT2	Data Line [Bit 2]	P12	D1-	Differential Data 1-
P03	D0+	Differential Data 0+	P13	CLK	Clock
P04	CD/DAT3	Card Detect/Data Line [Bit 3]	P14	D1+	Differential Data 1+
P05	D0-	Differential Data 0-	P15	VSS2	Supply Voltage Ground
P06	CMD	Command/Response	P16	VSS5	Supply Voltage Ground
P07	CD	Card Detect	P17	DAT0	Data Line [Bit 0]
P08	VSS1	Supply Voltage Ground	P18	DAT1	Data Line [Bit 1]
P09	VSS4	Supply Voltage Ground	P19	WP	Write Protect
P10	VDD2	Supply Voltage (1.8V)			

MATERIAL

- Housing: LCP
 - Contact: Phosphor Bronze
 - Shell: SUS
- Finish:
- Contact: Plated Gold in Mating Area ;
Tin Plated on Solder Balls ;
Nickel under plated overall
 - Shell: Nickel under Plated surface layer



Card Uninsertion	
Card Write Protect insertion	
Card Normal insertion	

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TOLERANCE UNLESS OTHERWISE STATED :
Up to 5 ±0.2
Above 5 ~ 15 ±0.3
Above 15 ~ 30 ±0.4
Above 30 ~ 50 ±0.5
Angle ±0.3°

3RD.
ANGEL'S



UNITS

MM

DRAWN BY:	DATE	MAT'L	TITLE	CONNECTOR
Jack Lu	05/21/26	FINISH	MODLE	SD4.0 短體板上型 H2.90 NON-PUSH
CHECKED BY:	DATE	SCALE	DWG NO.	SD4-004ZA4A-S502
Jacky Chen	05/21/26	1 : 1	PART NO.	SD4-004ZA4A-S502
APPROVED BY:	DATE	SHEET NO.	1 of 2	
Tony Kao	05/21/26			